

IP

Docket No.: 65933-079

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
Atsushi WADA, et al. : Confirmation Number: 4772
Serial No.: 10/808,566 : Group Art Unit: 2819
Filed: March 25, 2004 : Examiner: Not Yet Assigned
For: ANALOG-TO-DIGITAL CONVERSION CIRCUIT AND IMAGE PROCESSING
CIRCUIT FOR STEPWISE CONVERSION OF A SIGNAL THROUGH MULTIPLE
STAGES OF CONVERSION UNITS

TRANSMITTAL OF CERTIFIED PRIORITY DOCUMENT(S)

Mail Stop CPD
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

At the time the above application was filed, priority was claimed based on the following application(s):

Japanese Patent Application No. JP 2003-085314, filed on March 26, 2003.

Japanese Patent Application No. JP 2004-058546, filed on March 3, 2004.

A copy of each priority application listed above is enclosed.

Respectfully submitted,

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